राष्ट्रीय प्रौद्योगिकी संस्थान जमशेदपुर
National Institute of Technology Jamshedpur
Jamshedpur-831014, Jharkhand, India
(An Institution of National importance under MoE, Government of India)

इलेक्ट्रॉनिक्स एवं संचार इंजीनियरिंग विभाग / **Department of Electronics and Communication Engg.**

Advt. No.: NITJSR/ ECE/2023/161   Dated: November 28, 2023

Advertisement for Project Associate Position

A Walk-in-interview of eligible Indian citizens for the position of a **Project Associate** in Ministry of Electronics and Information Technology (MeitY) sponsored project under Chip to Startup (C2S) initiative in the Department of Electronics and Communication Engineering, National Institute of Technology Jamshedpur, Jamshedpur, India will be held as per the details mentioned below:

Last date to apply	**December 31, 2023, 5:00 PM**
Title of the Project	**Design and Development of SoC for Industry 4.0 with Functional safety and Security**
Project Code	**4197 (Capacity Building and Skill Development)**
Funding Agency	Ministry of Electronics and Information Technology (MeitY), Government of India
Name of the Post	Project Associate
No. of Posts	01 (one)
Duration	01 year or till the completion of the project, whichever is earlier? Initial appointment will be for one year, extendable upto 1 year subjected to satisfactory performance of the Project Associate and extension of the project.
Stipend	INR 45,000 per month (10% increment each year subject to satisfactory performance).
Investigator	**Dr. Amit Prakash,** Associate Professor, Department of Electronics and Communication Engineering, National Institute of Technology Jamshedpur, Jamshedpur-831014, India.
Co-Investigator	**Dr. Kunal Singh**, Assistant Professor, Department of Electronics and Communication Engineering, National Institute of Technology Jamshedpur, Jamshedpur-831014, India.
Essential Qualification	M. Tech with domain knowledge in VLSI design, EDA tools (Cadence, Synopsys and Mentor Graphics), RTL coding and physical design.
Preferable	Knowledge of RTL coding, driver development using C language, VLSI EDA tools and physical design. Applicants with a GATE score will be preferable.
Job Descriptions	✓ Carry out research in IC design, testing, and product development for Industry 4.0. ✓ Coordinate with cluster institutes for project activities and attend training. ✓ Maintain EDA tools and workstations in laboratories and support students in the related area. ✓ Create essential documentation for projects and manuals for EDA tools. ✓ Assisting PIs with various activities towards completion of the project.
Project Tenure	3 years

Benefits of joining the post: Candidate selected for this position can register for the Ph.D program provided she/he satisfies the eligibility criteria for Ph.D program in NIT Jamshedpur.

How to apply: Eligible and interested candidates are encouraged to send their application **as per the prescribed format in the website along with copies of all supporting documents as a single PDF file through Email to Dr. Amit Prakash (**amitprakash.ece@nitjsr.ac.in**) and Dr. Kunal Singh (**kunalsingh.ece@nitjsr.ac.in**) mentioning subject as "Application for Project Associate under (MeitY) sponsored project under Chip to Startup (C2S) initiative in the ECE Department, NIT Jamshedpur".**

Last date of receipt of application is December 31, 2023.

For more information, contact: +91- 9431905737, +91-8765504959

Please Note:

1. **Candidate should ensure their eligibility for the post before applying. Ineligible candidates will not be allowed.** Shortlisted candidates will be called for **walk-in interview.**
2. **The assignment is purely temporary in nature and is co-terminus with the project.**
3. **No TA/DA will be paid for appearing in the interview and/or joining the position.**
4. **All the terms & conditions for this recruitment will be as per the guidelines of DST/MeitY/MoE and NIT Jamshedpur. Institute reserves the right to cancel the recruitment without assigning any reason.**
5. **The selected candidate will be required to work in the Department of Electronics and Communication Engineering, NIT Jamshedpur as full-time research fellow.**
6. **Original documents of age proof/certificates/degrees/mark sheets and other testimonials must be presented at the time of interview.**

06/12/2023

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NATIONAL INSTITUTE OF TECHNOLOGY JAMSHEDPUR
(An Institution of National Importance under MHRD, Government of India)

Application for the Post of Project Associate in (MeitY) sponsored project under Chip to Startup (C2S) initiative for the Project Titled "Design and Development of SoC for Industry 4.0 with Functional safety and security"
(Project Code.: 4197)

Applicant's Details:

1.	Name		
2.	Father/Spouse's Name		PHOTO
3.	Mother's Name		
4.	Male/Female		
5.	Address	Correspondence:	Permanent:
6.	Contact Details	Email ID:	Phone Number:
7.	Age:	Date of Birth:	Age as on the closing date of Advt:
8.	Nationality:		Category: GEN/SC/ST/OBC (attach proof)
9.	Languages Known	Mother tongue:	Other languages:

10. Educational Qualifications:
(Supporting documents should be submitted along with the application as a single PDF file)

Degree/Certificate	University/Board/College	Year	Subjects	% Marks/CGPA	Class
10th					
12th					
B.Tech.					
M.Tech.					

GATE details: Subject of examination: Percentile: (Please attach score card)	Qualifying Year:	All India Rank: Score:

11. Any other relevant information?

12. Declaration: I hereby declare that the entries made in this application form are correct to the best of my knowledge and belief.

Place:

Date: (Signature of the applicant)