

National Institute of Technology, Jamshedpur

Jamshedpur-831014, Jharkhand (State), India
(An Institution of National Importance under MHRD, Government of India)

Ref. No.: NITJSR/EEE/32/2017

Date: 2-1-2017

NOTICE

Mrs. Rashmi Sinha (Roll No.- 2009RSEC003), Research Scholar of Department of Electronics and Communication Engineering, is going to defend her PhD thesis on the topic **“Soft Computing Approach to Design a Novel and Efficient Adaptive Equalizer for Linear and Nonlinear Channels”** on 10-11-2017 (Friday) at 2PM in Micro Processor lab (NA-113), Department of Electronics and Communication Engineering of the institute.

All member of DSC, Members of DAC (PG & R), interested faculty members, Research Scholars and Students are cordially invited to attend the seminar.

Members of the DSC are:

1. Head, EEE - Chairman
2. Dr. D. A. Khan, Department of DCA - Member
3. Dr. Ramayan Singh, Department of Maths - Member
4. Prof. (Dr.) S. N. Singh, ECE - Member
5. Prof. (Dr) A. Choubey, ECE - Supervisor & Convenor

Niranjan Kumar
02-11-2017
(Niranjan Kumar)
DSC Chairman &
Head, EEE

COPY TO:

- I. Director for kind information
- II. Dean (Academic Affairs)
- III. Dean (Admin & F.A, R & C)
- IV. The Registrar
- V. The Finance Officers
- VI. All HOD's with request for circulation among the faculty members
- VII. All members of DSC
- VIII. P/I Website
- IX. Mrs. Rashmi Sinha