



National Conference on Electronics, Communication and Computation-2020
Online Virtual Conference
 5-6 September, 2020
 NIT Jamshedpur, Jharkhand, India
NCECC-2020



Theme: Role of Electronics and Communication in pandemic situation

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IMPORTANT DATES

Paper Submission: 20-8-2020
 Notification of Acceptance: 31-8-2020
 Final Paper Submission: 3-9-2020
 Registration Deadline: 3-9-2020

Website: www.nceccnitjsr.in

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Prof. Rajeev Tripathi
 Director, MNNIT Allahabad
 Chief Guest
 Inaugural Session



Prof. Gopal Phatak
 VC, Jharkhand Technical University
 Chief Guest
 Valedictory Session



Worapong Tangsrirat King
 Mongkut's Institute of Technology Thailand
 Guest of Honour
 Valedictory Session

National Conference on Electronics, Communication and Computation (NCECC-2020) will be held on 5-6 September 2020 at National Institute of Technology Jamshedpur. NIT Jamshedpur is an institute of National Importance and secured rank-79 in NIRF Ranking 2020. NCECC-2020 will provide a platform for scientists, researchers, academicians and professionals across the country for dissemination of new research ideas in various areas of electronics, communication and computation. This conference is being sponsored by TEQIP-3 (a Government of India Project assisted by World Bank).

In addition to the parallel online technical sessions, the conference program will include world-class keynotes, industry sessions and panels, by renowned experts.

In wake of pandemic situation, the conference will be conducted online platform.

Authors were invited to submit their original unpublished papers, in all areas of electronic devices, communication systems, signal processing, embedded systems and computation engineering. In all 108 papers were submitted out of which 68 have been selected for presentation.

NCECC-2020 Schedule

Day-1 (5-9-2020) Inaugural Session (9:00-10:00 AM) Chief Guest: Prof. Rajeev Tripathi, Director, MNNIT Allahabad	
Keynote Lecture-1 (10:00-11:00 AM) Prof. TK Bhattacharya, IIT Kharagpur	
Technical Session-1 (11:00 AM-12:30 PM) (Track: Embedded Systems)	Technical Session-2 (11:00 AM-12:30 PM) (Track: Communication Systems-1)
Technical Session-3 (1:00-2:30 PM) (Track: Embedded Sys. & Computation)	Technical Session-4 (1:00-2:30 PM) (Track: Image Processing)
Keynote Lecture-2 (3:00-4:00 PM) Dr. S P Sagar, NML, Jamshedpur	
Technical Session-5 (4:00-5:30 PM) (Track: Signal Processing)	Technical Session-6 (4:00-5:30 PM) (Track: Electronic Devices-1)
Day-2 (6-9-2020) Keynote Lecture-3 (9:00-10:00 AM) Prof. S Jit, IIT (BHU) Varanasi	
Technical Session-7 (10:15-11:45 AM) (Track: Electronic Devices-2)	Technical Session-8 (10:15-11:45 AM) (Track: Communication Systems-2)
Keynote Lecture-4 (12:00-1:00 PM) Prof. R P Yadav, MNIT Jaipur	
Technical Session-9 (1:00-1:45 PM) (Track: Communication Systems-3)	
Valedictory Session (3:00-3:45 PM) Chief Guest: Prof. G Pathak, VC, Jharkhand Technical University, Ranchi	

Keynote Speaker



Prof. Satybrat Jit
 Professor, IIT (BHU) Varanasi



Prof. T K Bhattacharya
 Professor, IIT Kharagpur



Dr. S P Sagar, Sr. Principal Scientist,
 CSIR-NML, Jamshedpur



Prof. R P Yadav
 Professor, MNIT Jaipur

Session Chairs



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 Asso. Professor, IIT Bhubaneswar



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 Professor,
 ADGITM New Delhi



Prof. N Biradar, Principal,
 BKIT Bidar

