

About Institute and Jamshedpur

National Institute of Technology Jamshedpur was originally founded as Regional Institute of Technology in 1960. Later upgraded to National Institute of Technology on 27th December 2002, with the status of Deemed University. It is now totally under the control of Ministry of Human Resources Development (MHRD), Government of India, New Delhi, since April 2003.

About the Department

The Department of Electrical & Electronics Engineering was started in 1960. The Department has been consistently producing quality Electrical Engineers since its inception and is also involved in research and development activities. In addition to the UG program the department runs two PG programs and Ph.D. program in different areas of specialization.

Purpose of ETFCSOC

The main objective of this STC is to enhance the knowledge of the participants in the recent trends of power system operation and control along with discussions on future challenges which will face by the power systems. This program will also allow us to share experiences among the engineers, academicians and research students working in the relevant areas.

ETFCSOC contents

The ETFCSOC includes the following key topics:

- REC mechanism for transaction of renewable
- Planning and design aspects of power distribution systems
- Market power in deregulated electricity markets
- Flexible AC Transmission Systems
- Wide area measurement systems & Power system dynamics and control
- Advanced Controller Design Techniques and their Applications
- Transmission network planning for eastern region of India
- Modern Power Electronics Converter and AC drives
- Performance and Power quality improvement of Induction Motor Drives systems

Registration Fees & Selection

There will be a registration fee for sponsored participants from Academic Institutions and Industry. Registration fee can be paid by sending crossed demand draft in favor of "Director NIT Jamshedpur", payable at Jamshedpur along with registration form.

Participants from:

Academic Institutions	Rs. 1500/-
Industry and R&D	Rs. 2500/-
Research Scholars/ M. Tech Students	Rs. 750/-

No fee will be charged from the internal faculty members and Rs. 500 will be charged from internal Research Scholars/ M. Tech Students.

Accommodation

No DA/TA will be provided to participants for attending the STC. Accommodation will be provided for out station participants on prior confirmation and subject to availability and on prior payment.

Eminent Speakers

Resource persons are renowned faculties from IITs & NITs

Important Dates

Last date for receiving application	June 10, 2016
Notification about selection	June 13, 2016
Confirmation from the participant	June 17, 2016

Coordinator

Dr. Niranjan Kumar
Head, Department of Electrical & Electronics Engineering
National Institute of Technology Jamshedpur

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Note: All email correspondence must be addressed with the subject ETFCSOC - 2016.

One Week
Short Term Course
on

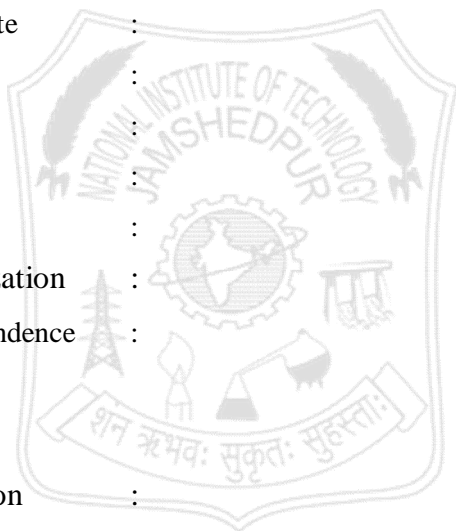
Emerging Trends and Future Challenges in Power System Operation and Control

(ETFCPSOC – 2016)

June 20 – 24, 2016

Registration Form

Name of the Candidate :
Designation :
Discipline/Branch :
E-mail :
Contact Number :
Name of the Organization :
Address for correspondence :



Highest Qualification :

Experiences (in years) Teaching : Industrial or Others :

Demand Draft Number : Date :

Bank & Branch : Rupees:

Date : Signature of the Candidate

Name, Signature and seal of the sponsoring authority (HOD/Principal/Head of the Institution/Organization):

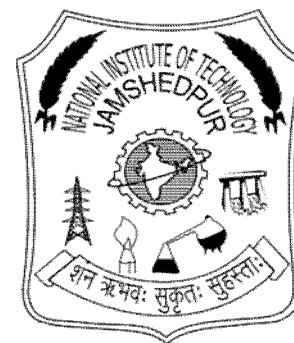
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Organized by:
Department of Electrical & Electronics Engineering
National Institute of Technology Jamshedpur
Jamshedpur – 831014, Jharkhand
India
www.nitjsr.ac.in